



2124

PATENT
5181-48500/P4505

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 09/553,970
Filed: April 21, 2000
Inventors:
Allavarpu, et al.

Examiner: Gubiotti, Matthew P.
Group/Art Unit: 2124
Atty. Dkt. No: 5181-48500

Title: Thread-Safe Remote
Debugger

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Robert C. Kowert

Name of Registered Representative

[Signature]

August 5, 2003

Signature

Date

#2/a
S. Calho
8-13-03

RESPONSE TO OFFICE ACTION OF
MAY 5, 2003

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Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Technology Center 2100

Dear Sir:

This paper is submitted in response to the Office Action of May 5, 2003, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.



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Technology Center 2100

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S): Sai V. Allavarpu, Rajeev Angal, and Tony T. Vuong
ASSIGNEE: Sun Microsystems, Inc.
SERIAL NO.: 09/553,970
FILING DATE: April 21, 2000
TITLE: Thread-Safe Remote Debugger

ATTORNEY DOCKET: 5181-48500/P4505

The date stamp of the mail room of the U.S. Patent and Trademark Office hereon will acknowledge receipt of the attached 1) Information Disclosure Statement w/accompanying Form PTO-1449 and references A1 - A20; 2) Copies of specifications referenced in paragraph 3; and 3) Return Postcard.

RCK/dmp

Via First Class Mail

date: Jan. 23, 2002

